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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Richard Pierre FOURNEL, Serge FRUHAUF, and Francois TAILLIET

Serial No:

Unassigned Herewith

Filed: For:

PHYSICAL FUSE FOR SEMICONDUCTOR INTEGRATED

CIRCUIT

Examiner:

Unassigned

Art Unit:

Unassigned

Confirmation No.: Unassigned

Mail Stop Reissue Commissioner for Patents P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

PRELIMINARY AMENDMENT

Prior to examination, please amend the above-identified application as follows: